

FIG. 1

data recording device

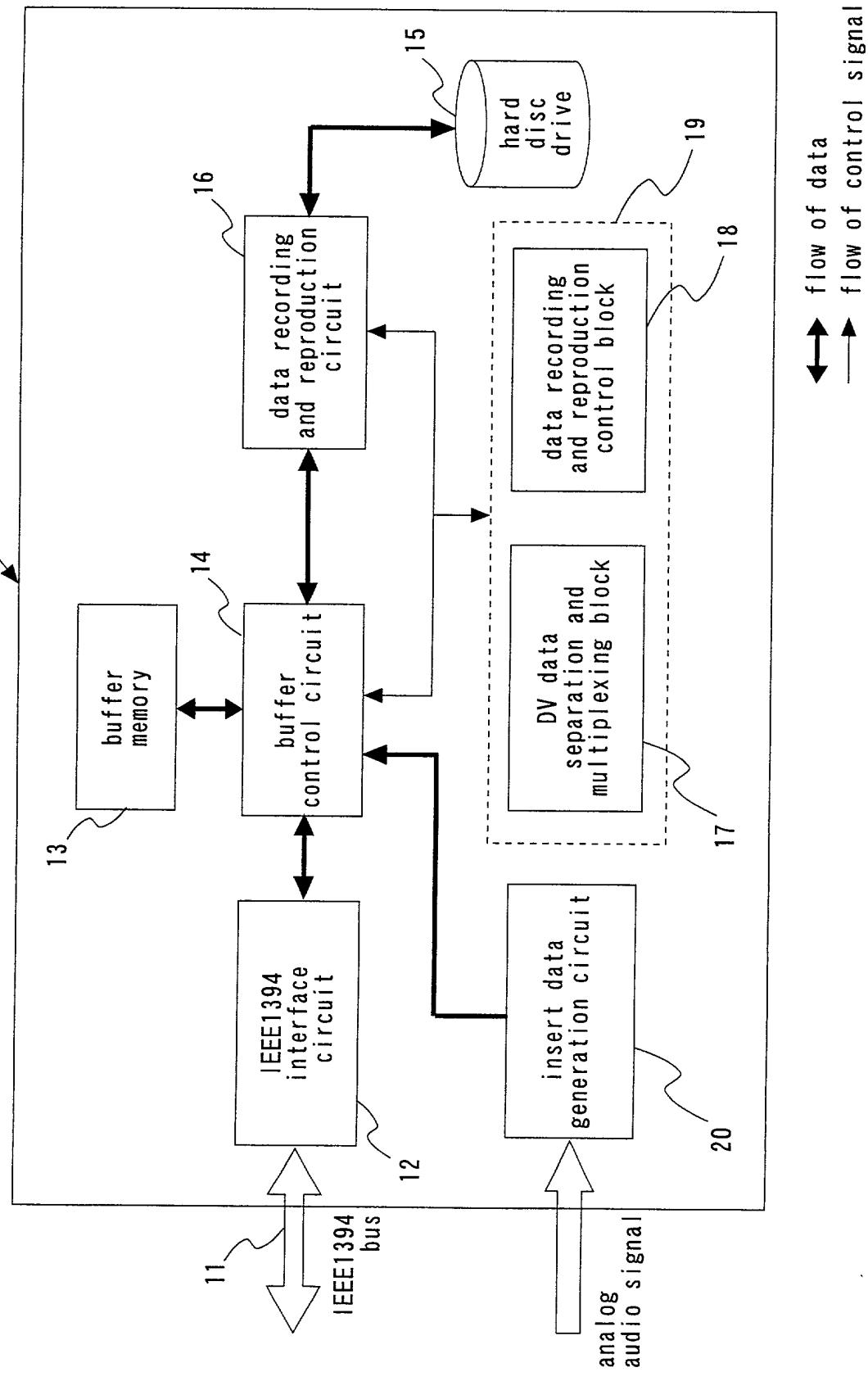


FIG. 2

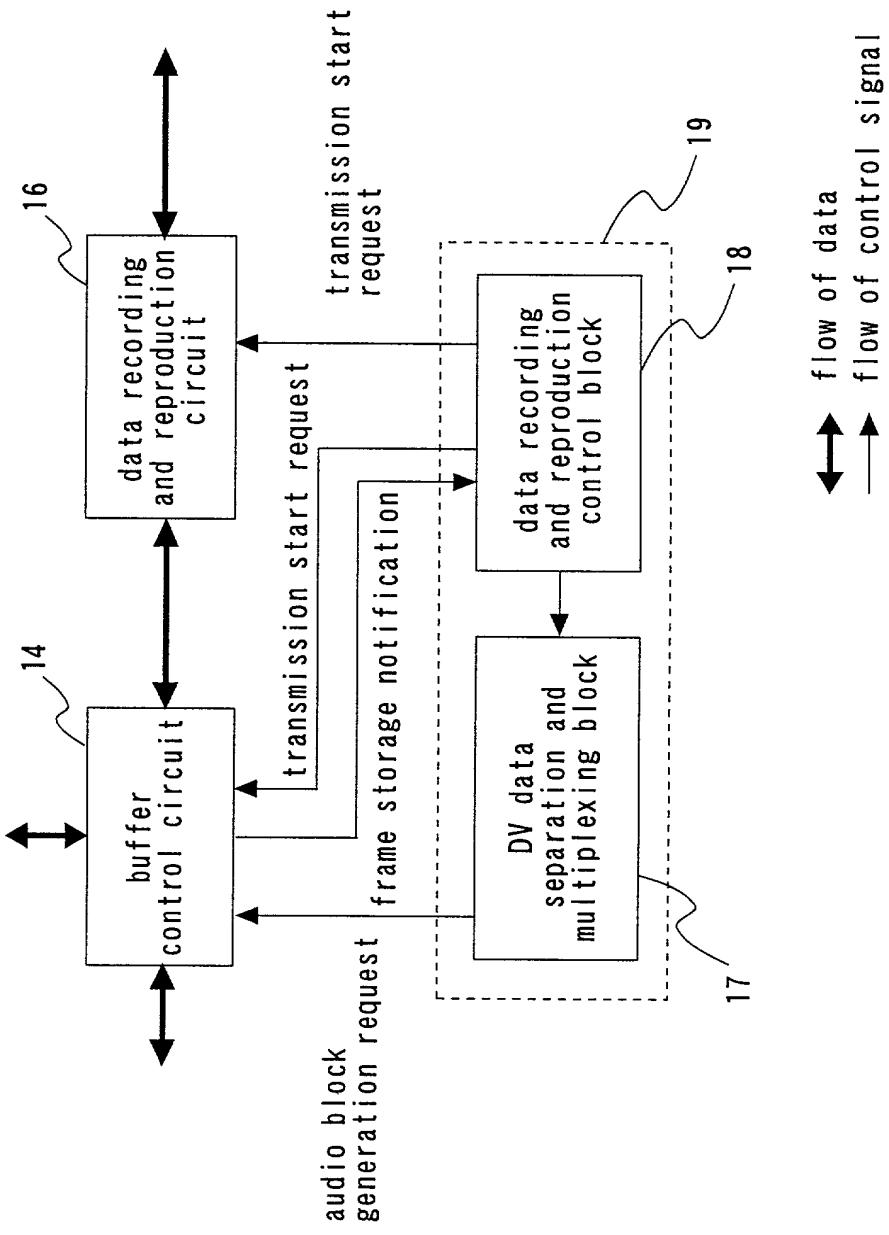
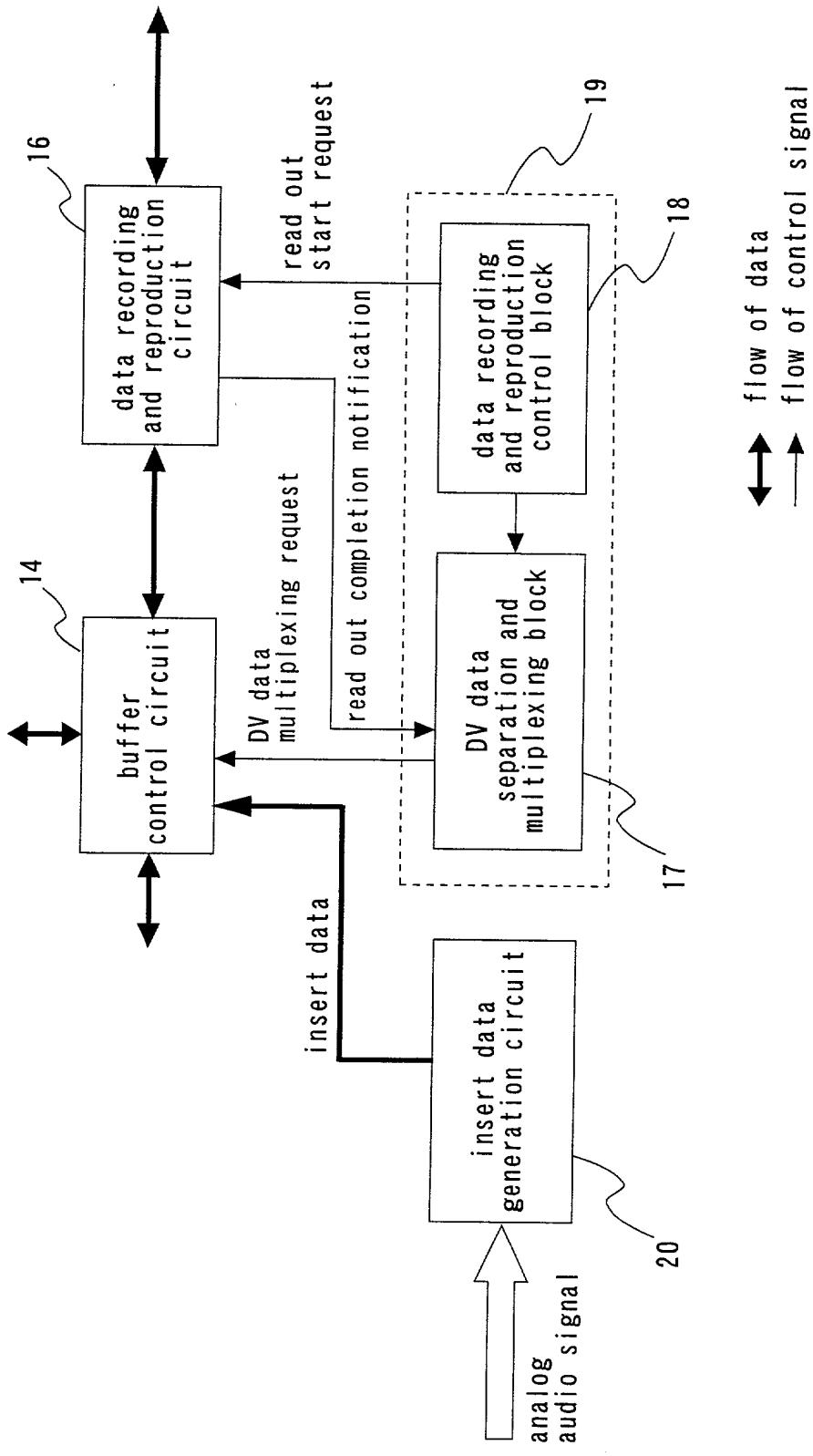
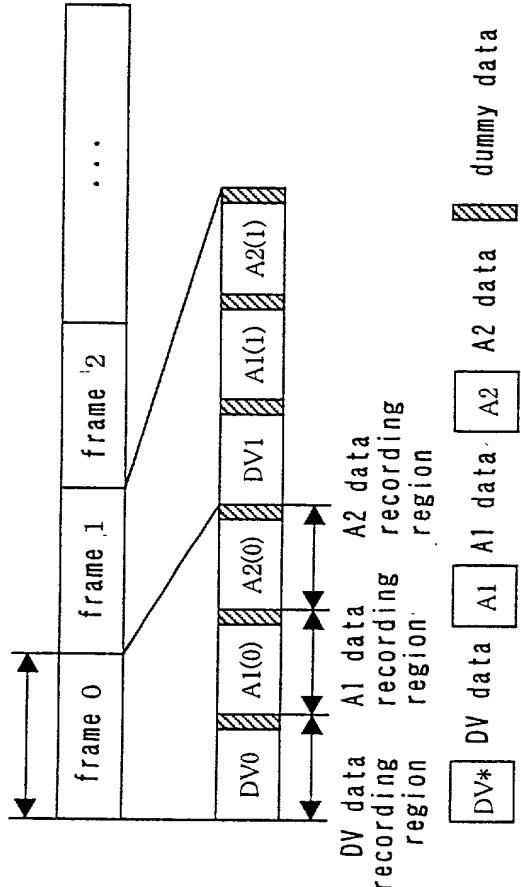


FIG. 3

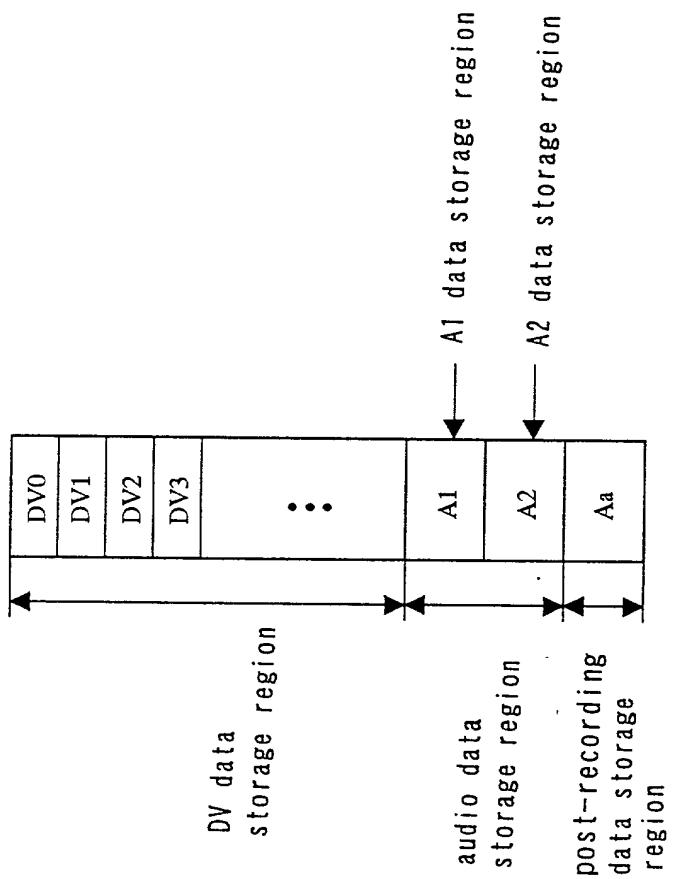


F I G. 4

DV frame recording region



F I G. 5



F I G. 6

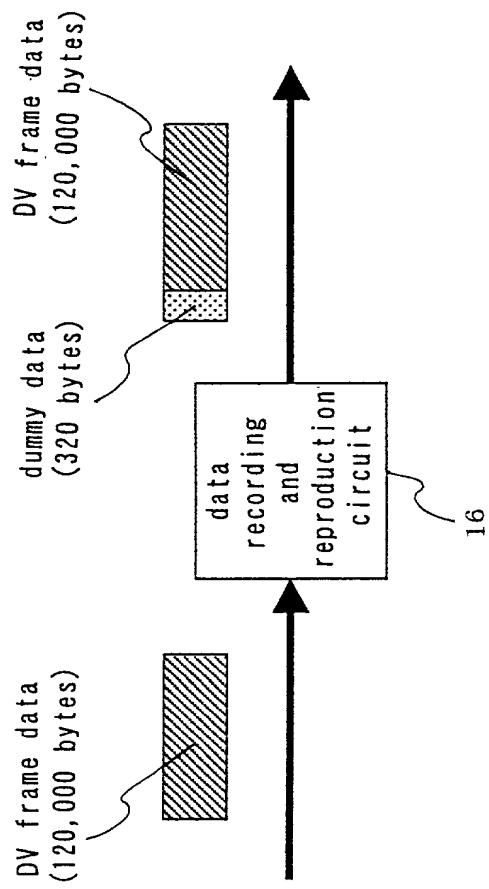


FIG. 7

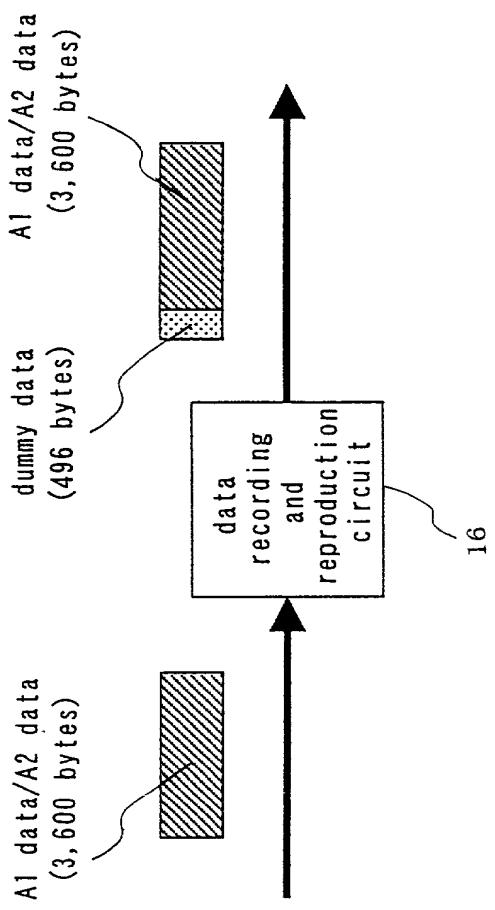
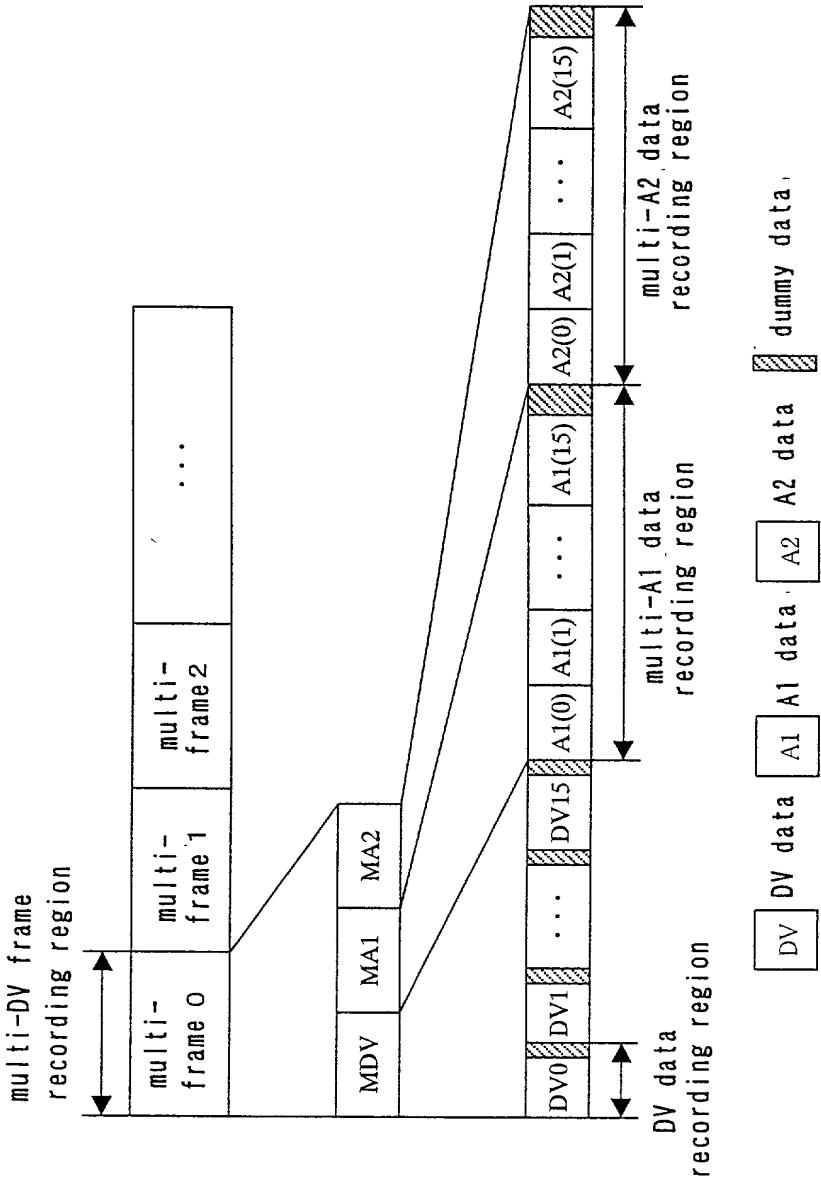


FIG. 8



F I G. 9

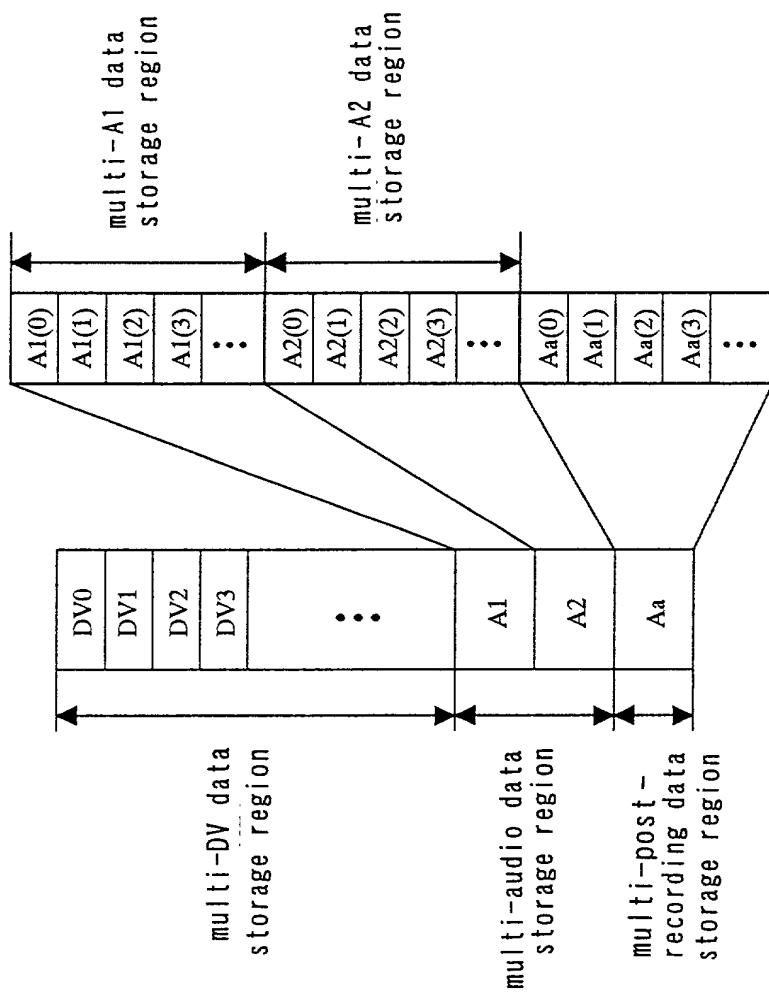


FIG. 10

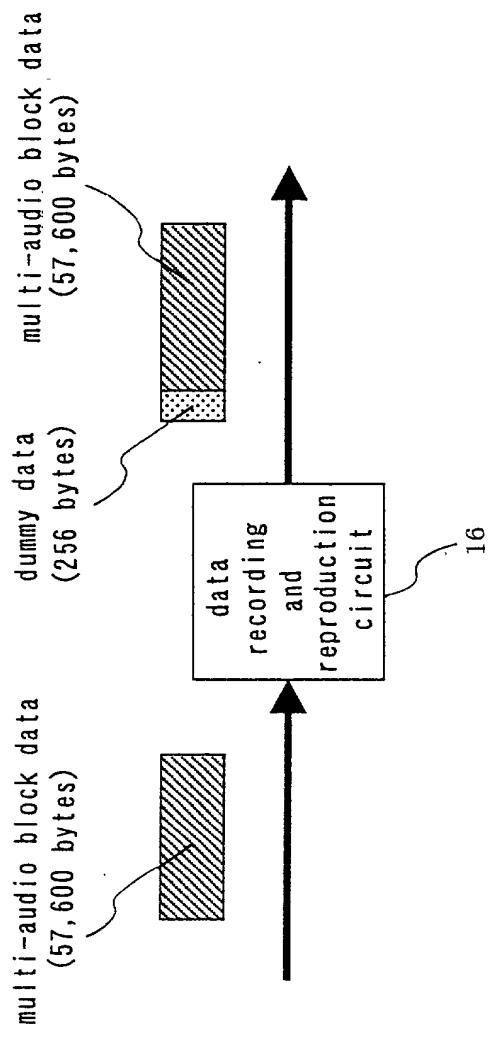


FIG. 11

data recording device

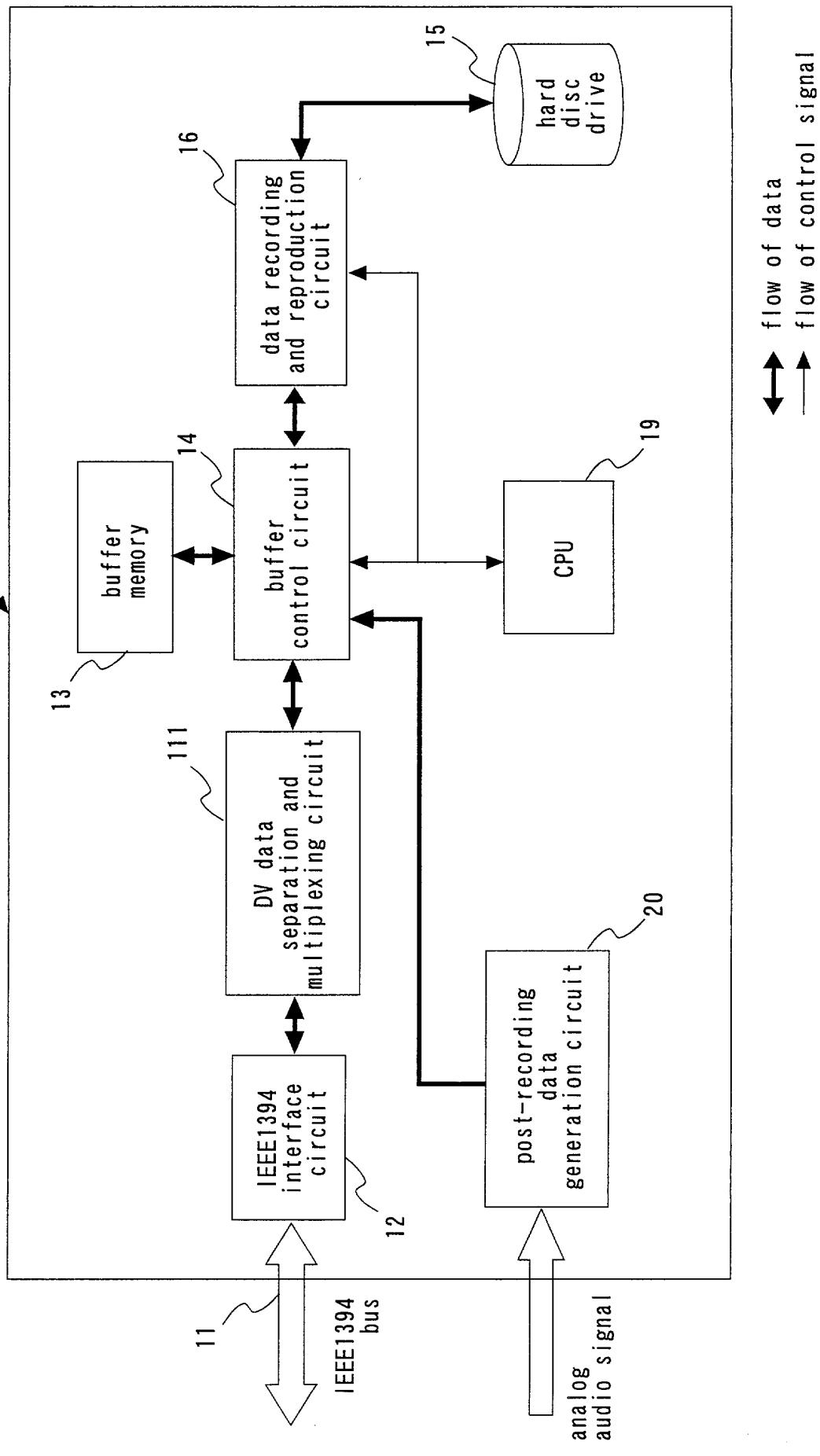


FIG. 12

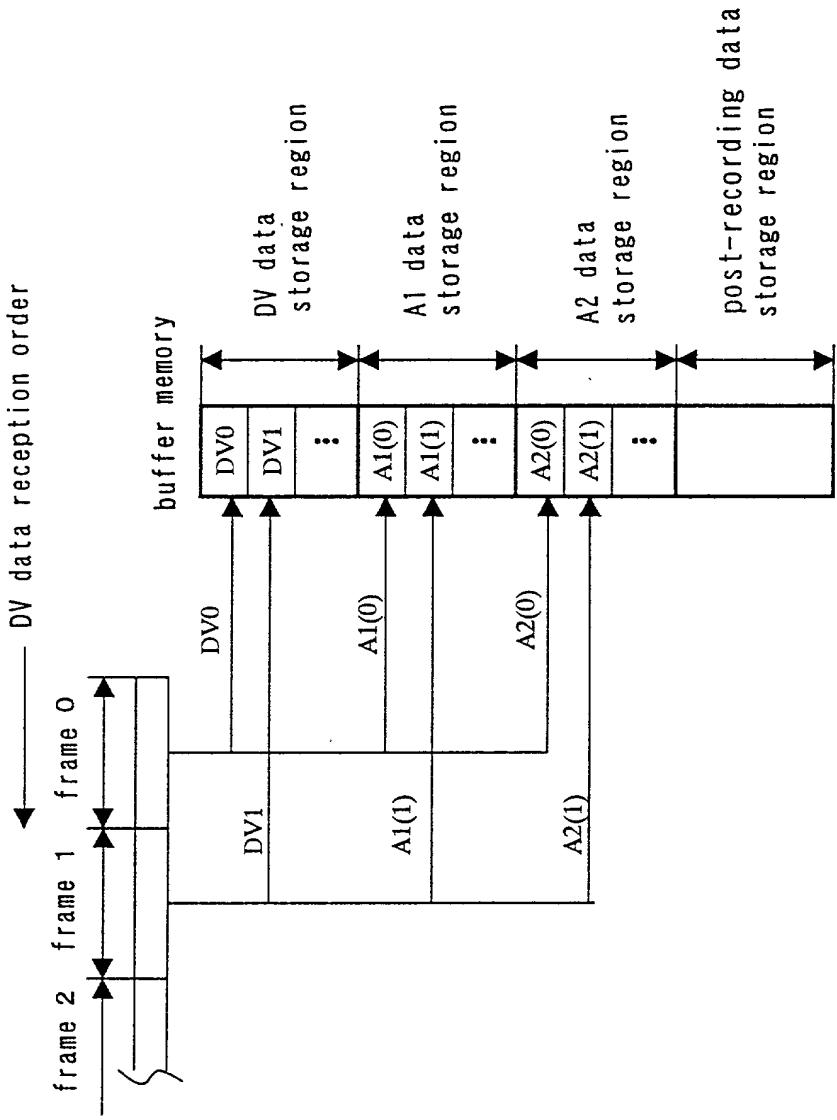
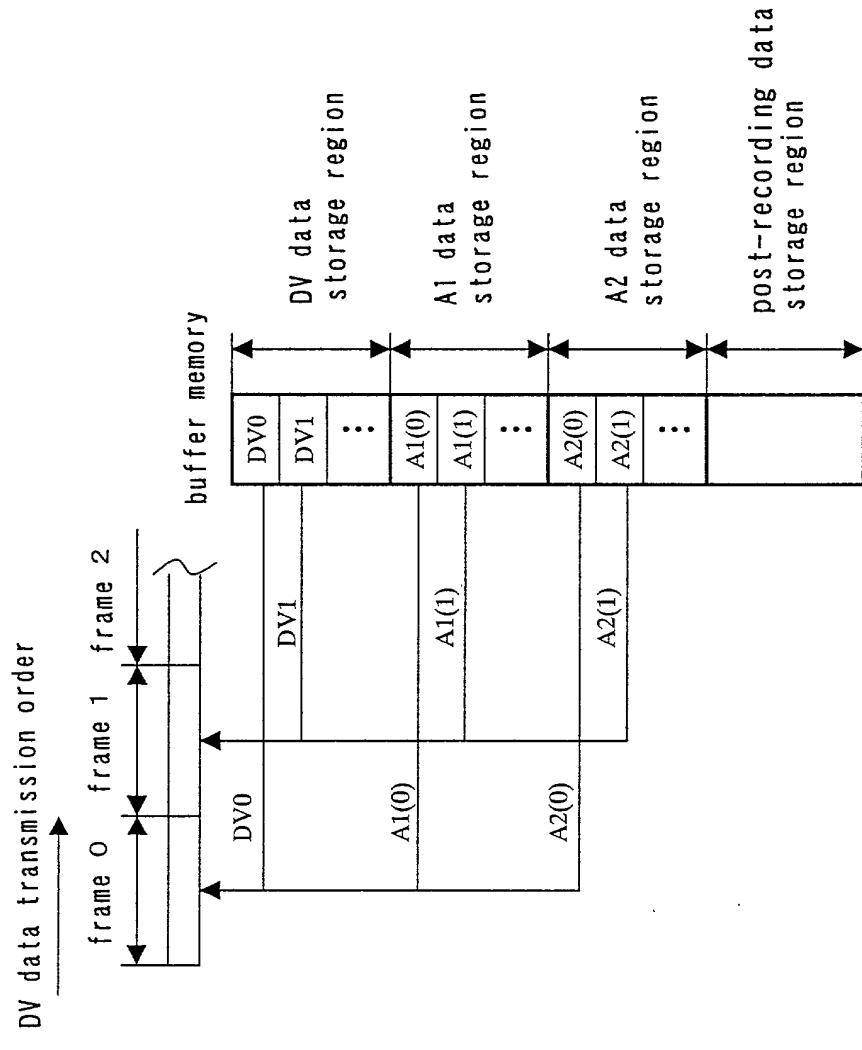


FIG. 13



F | G. 14

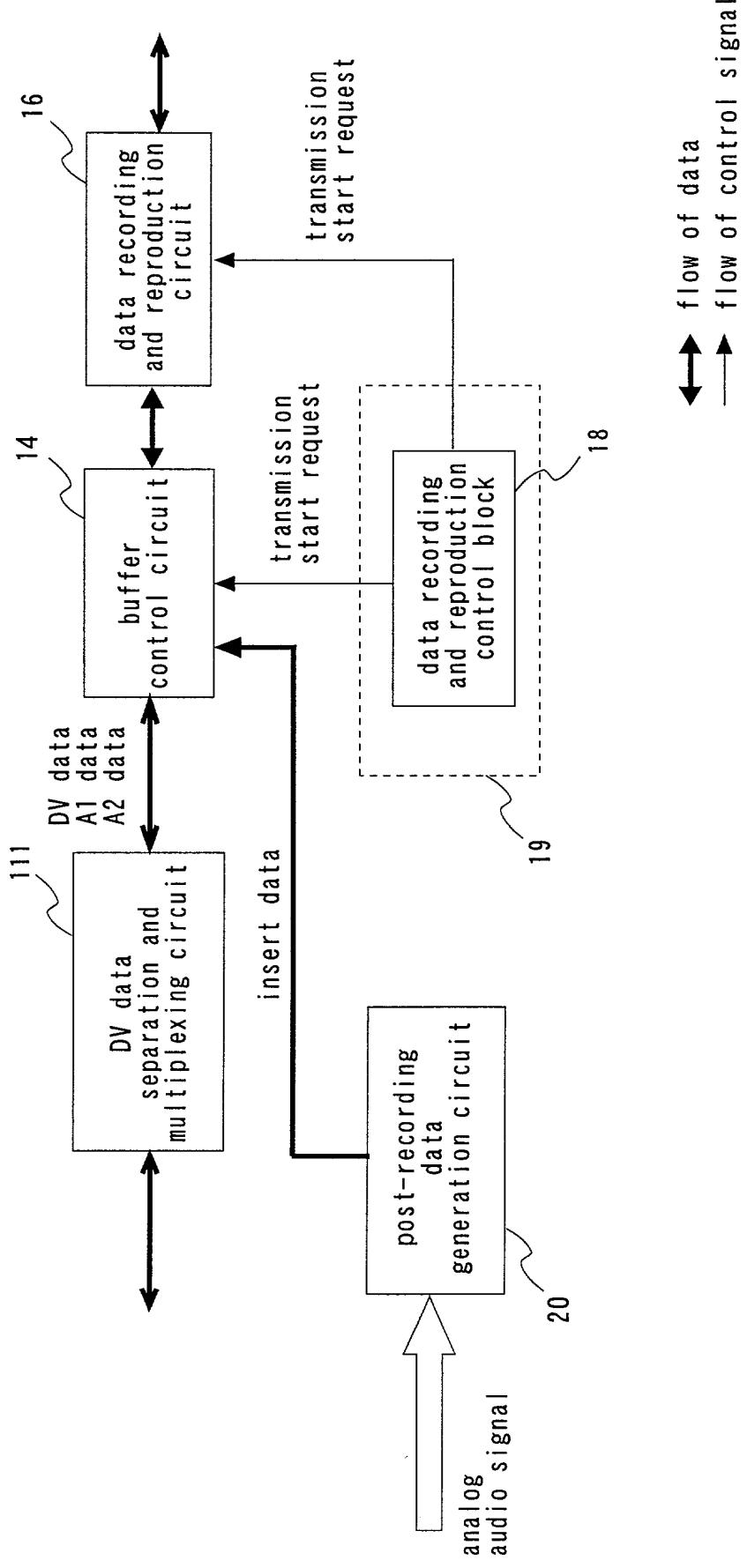


FIG. 15

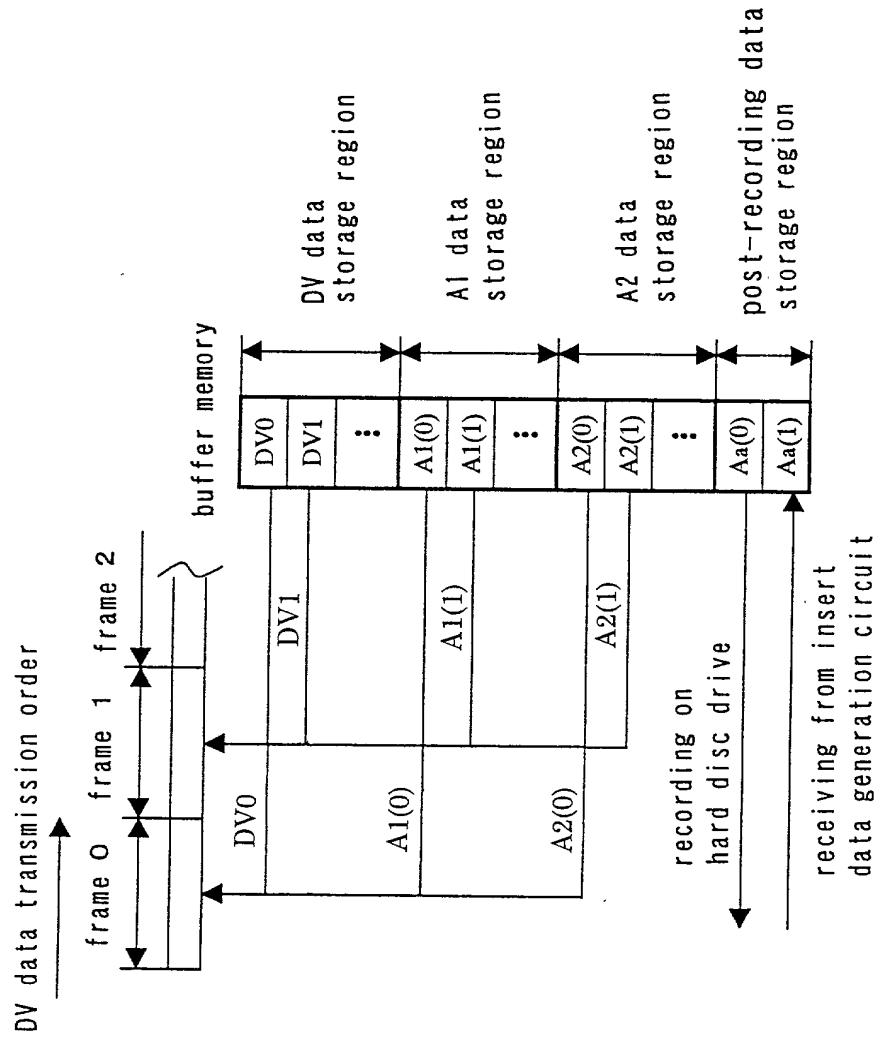
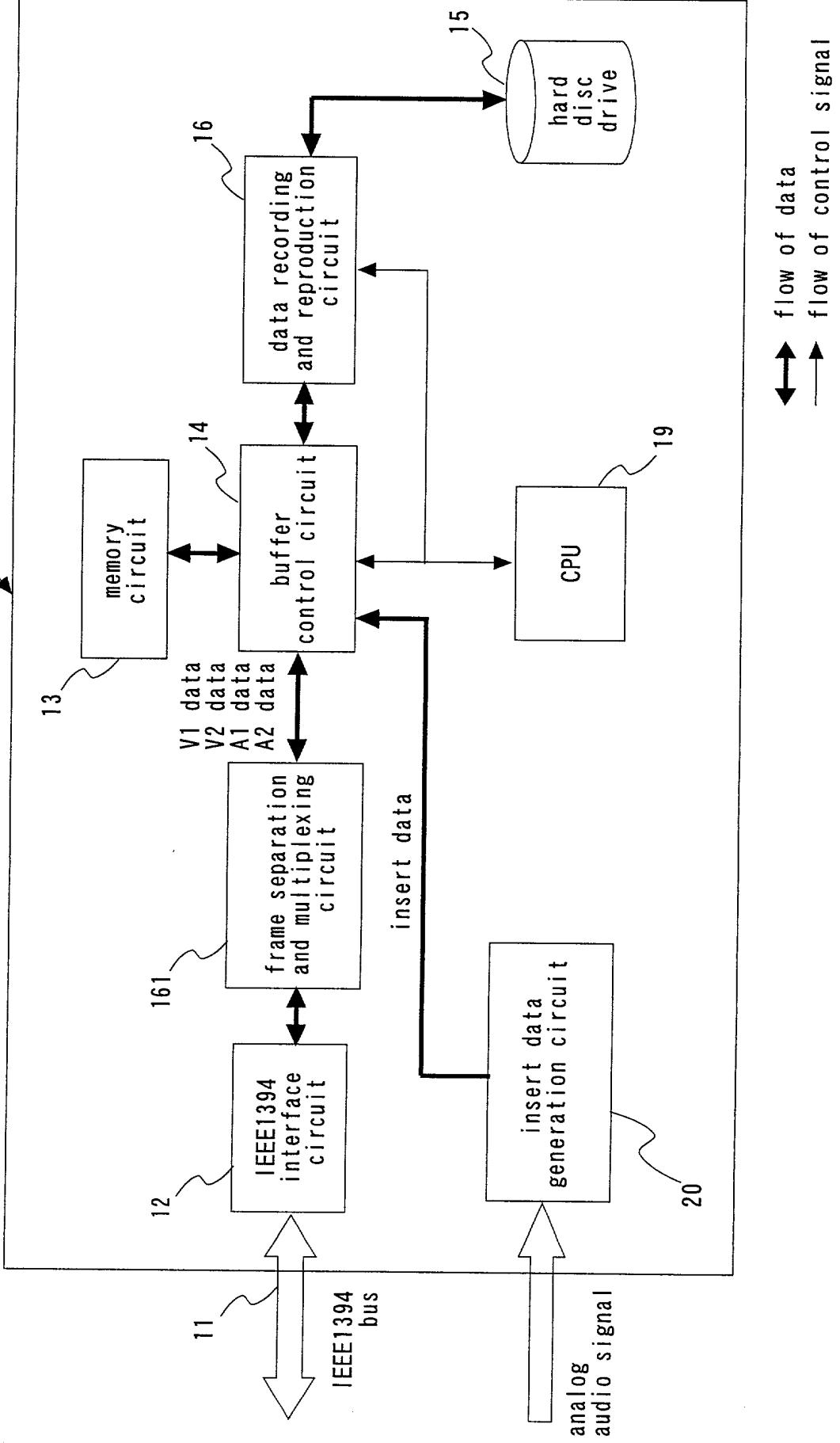
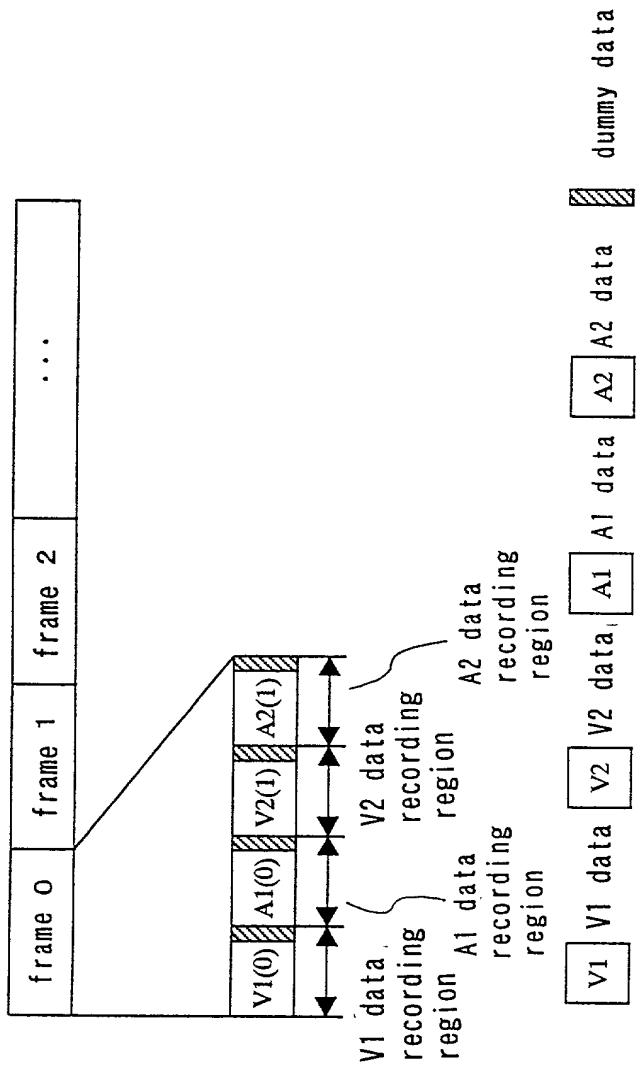


FIG. 16

data recording device



F I G. 17



F I G. 1 8

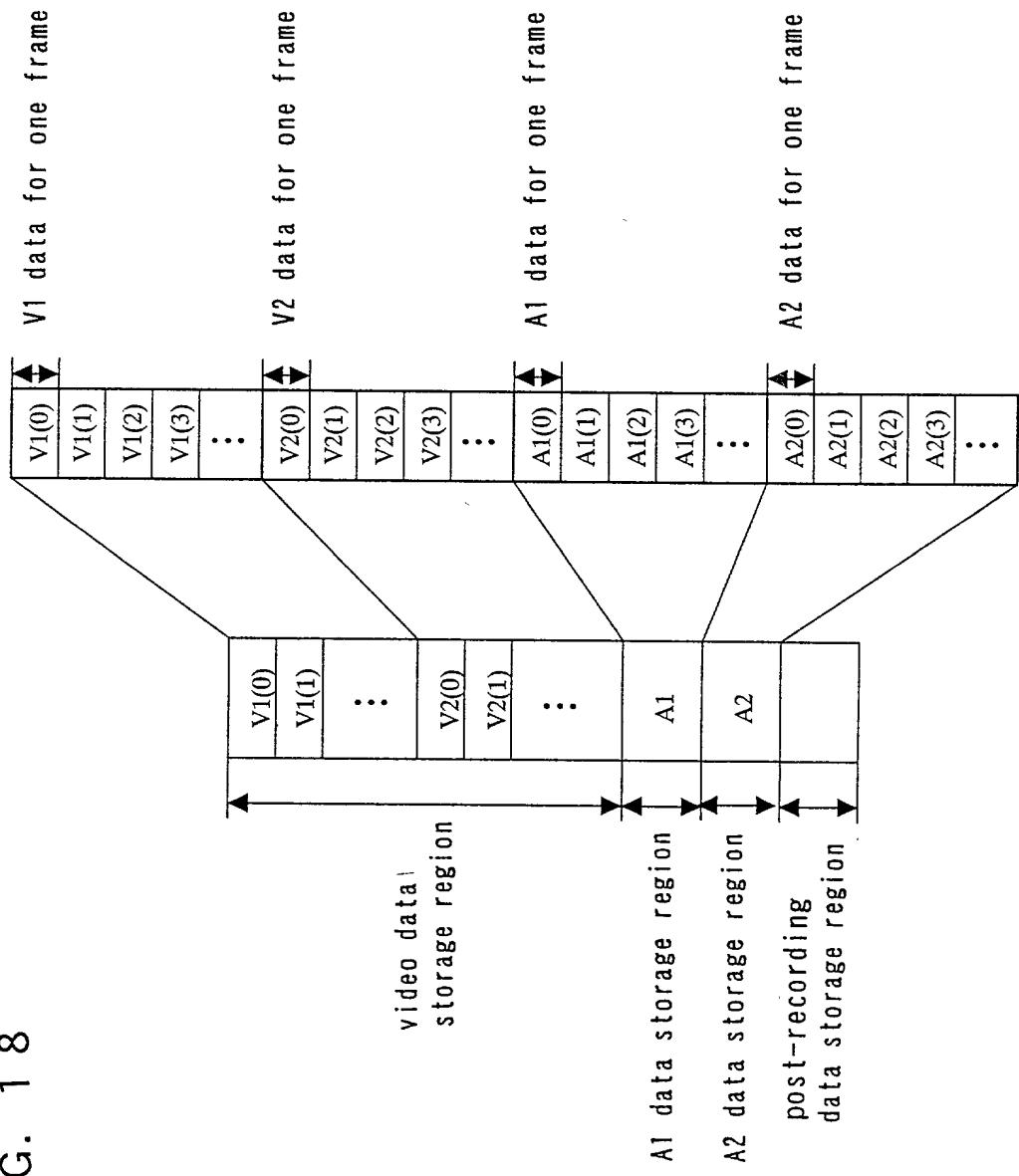


FIG. 19

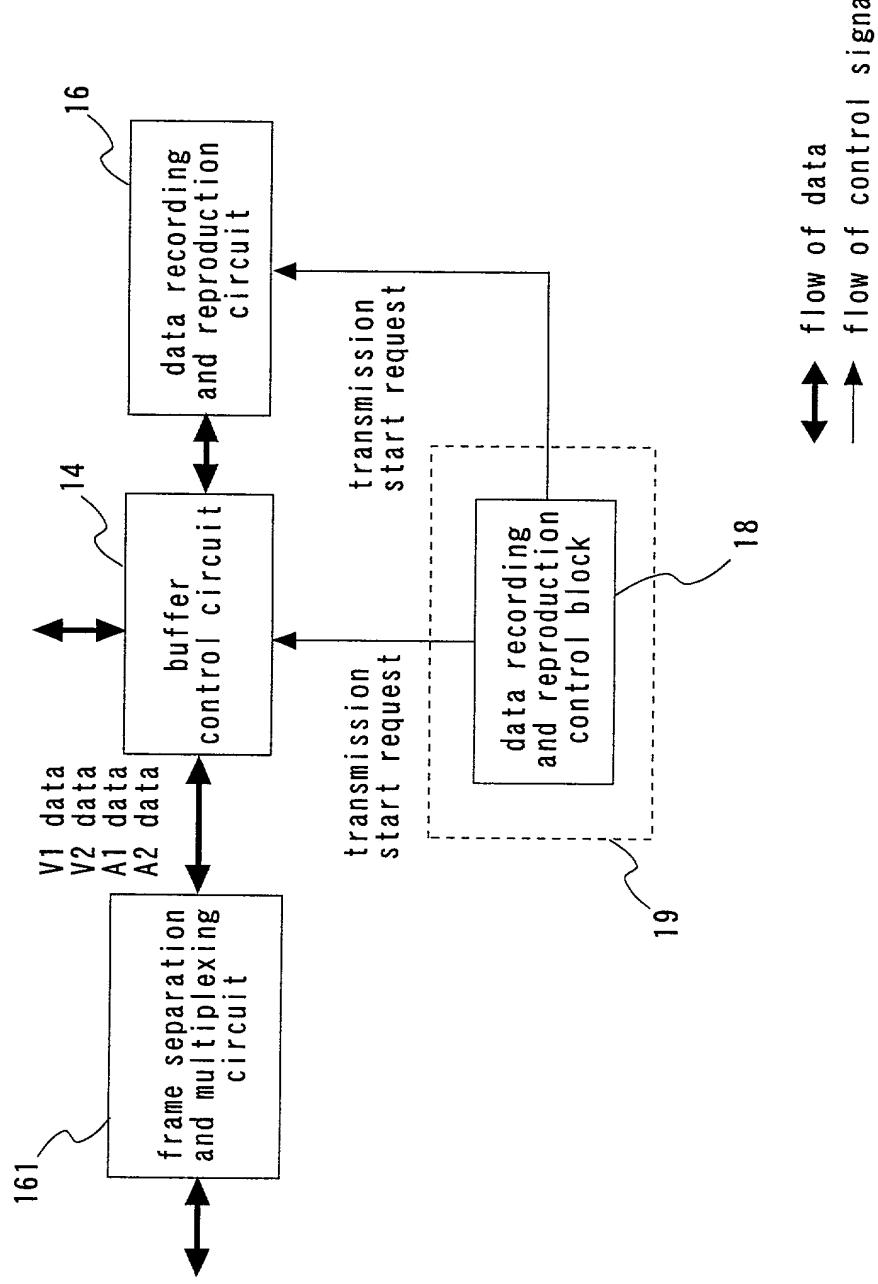


FIG. 20

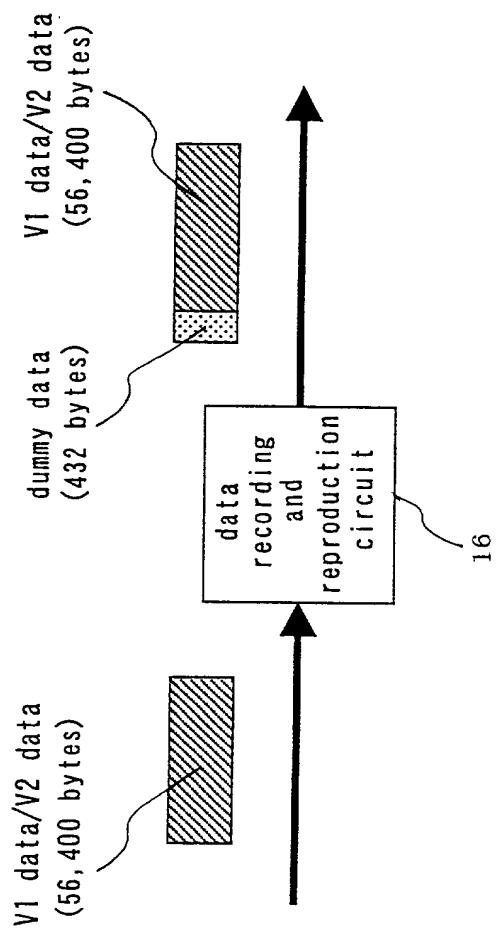


FIG. 21

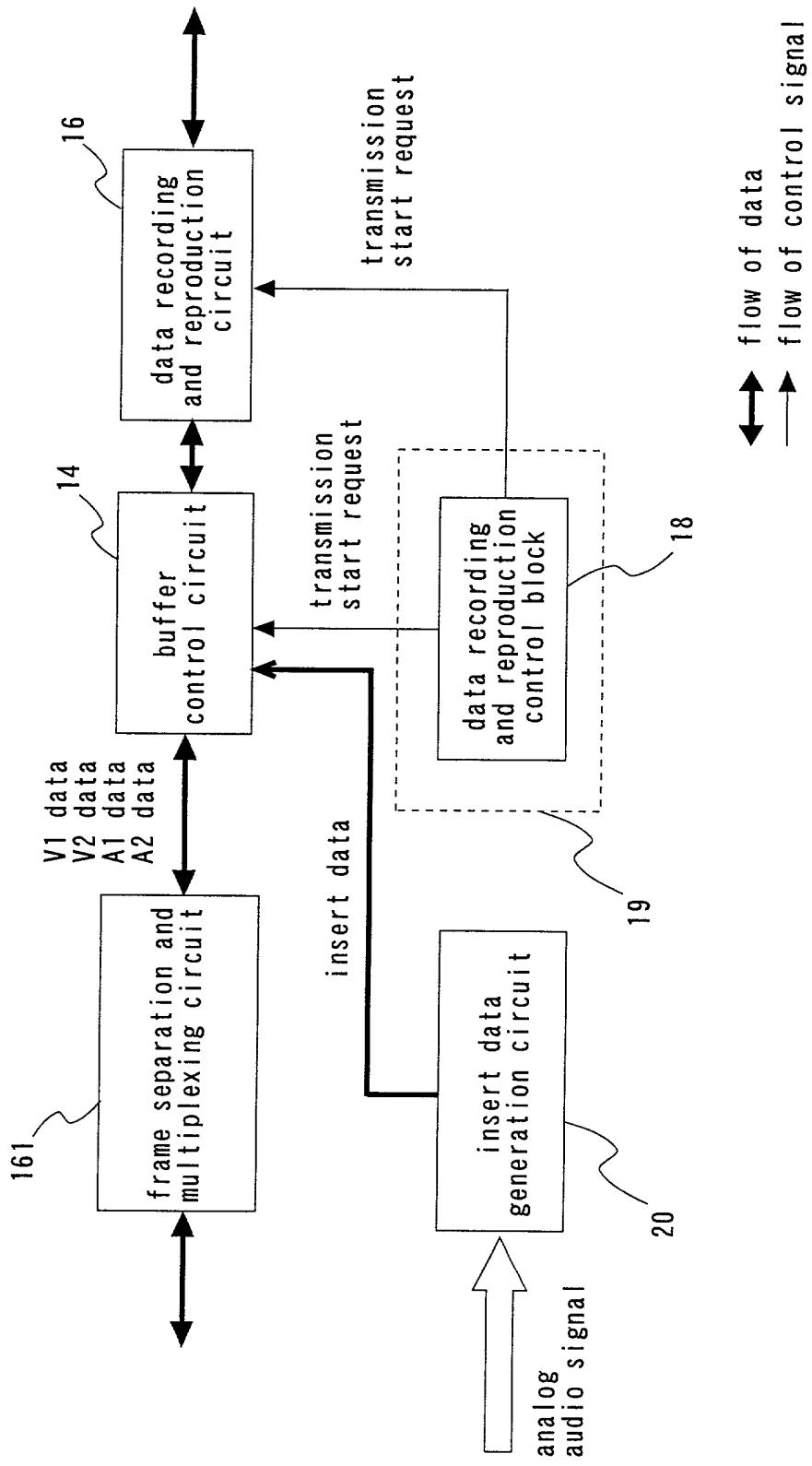


FIG. 22

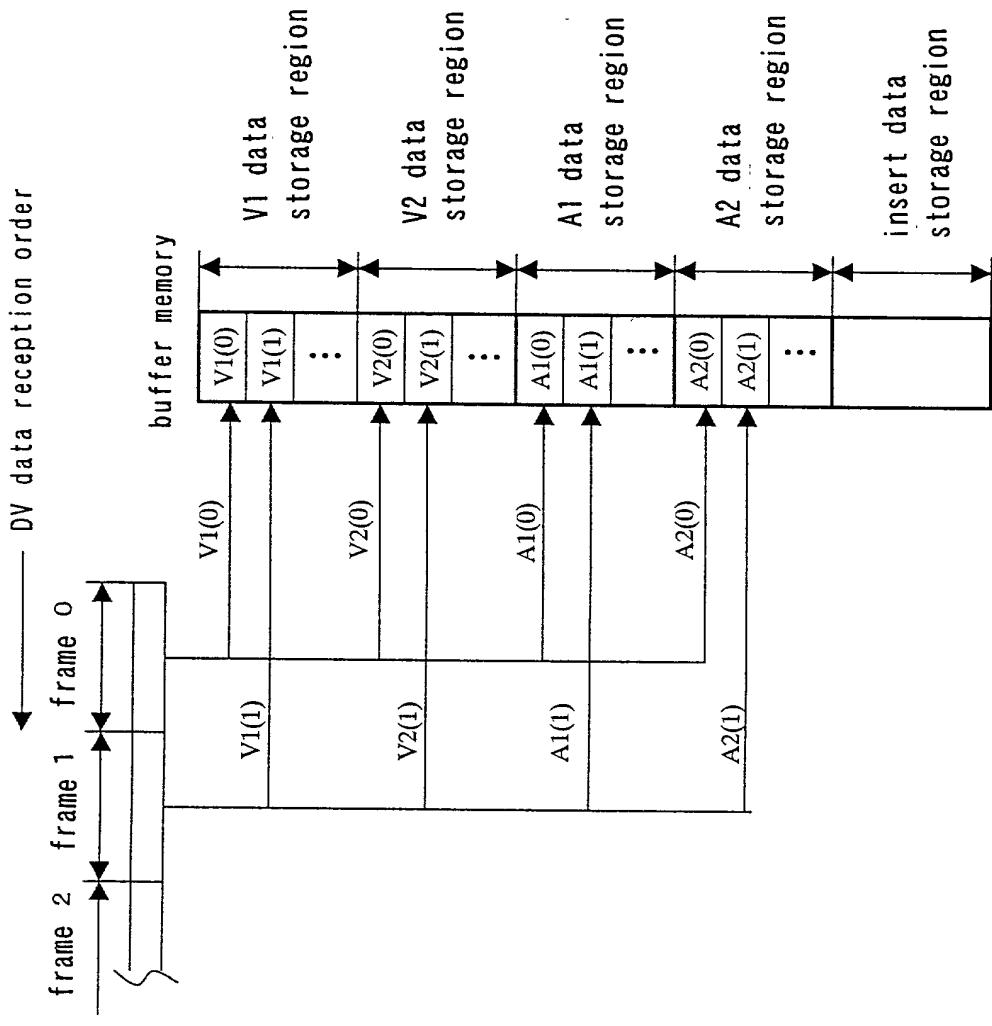


FIG. 23

DV data transmission order

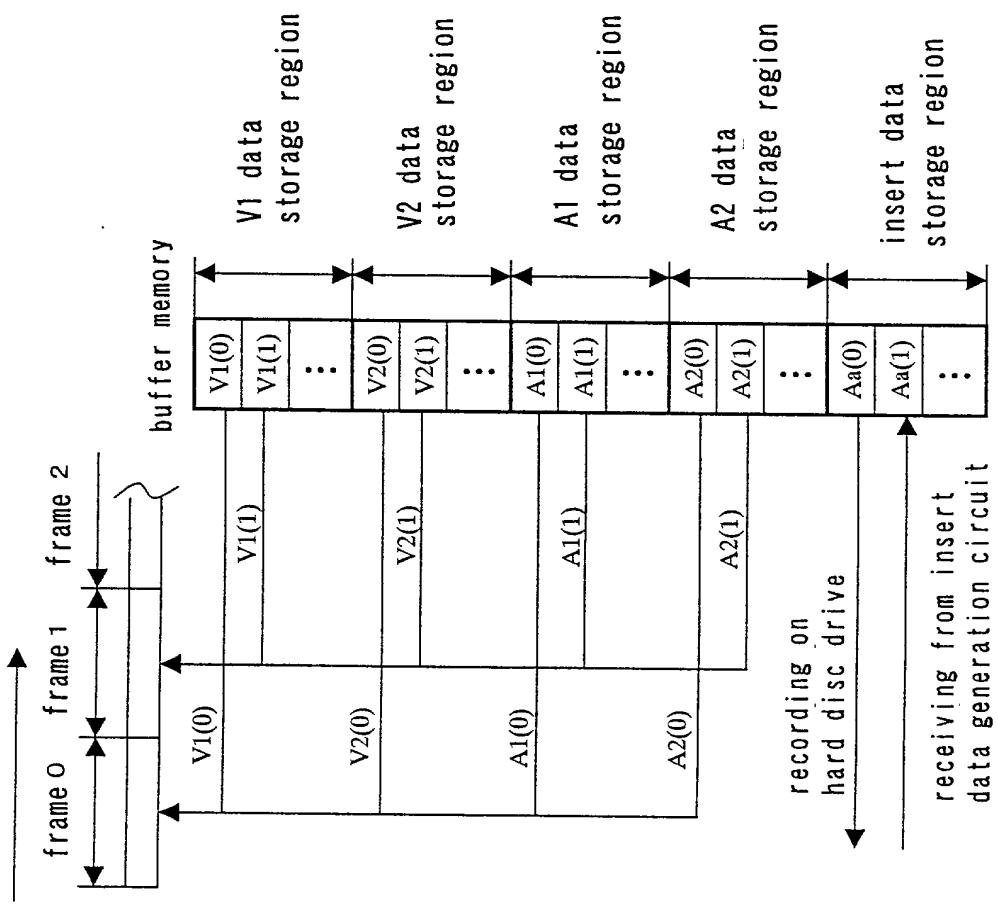
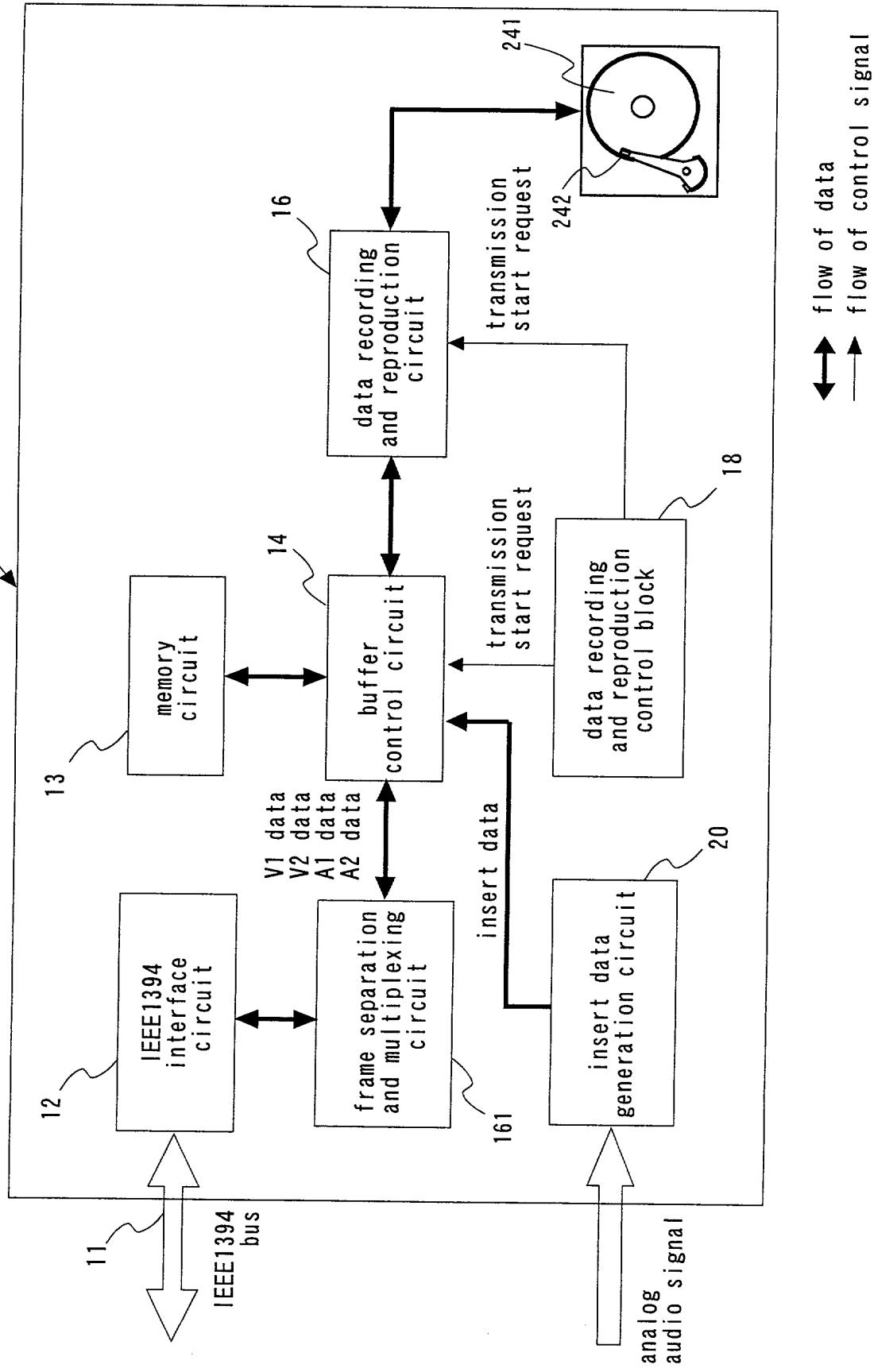


FIG. 24

data recording device



F | G. 25

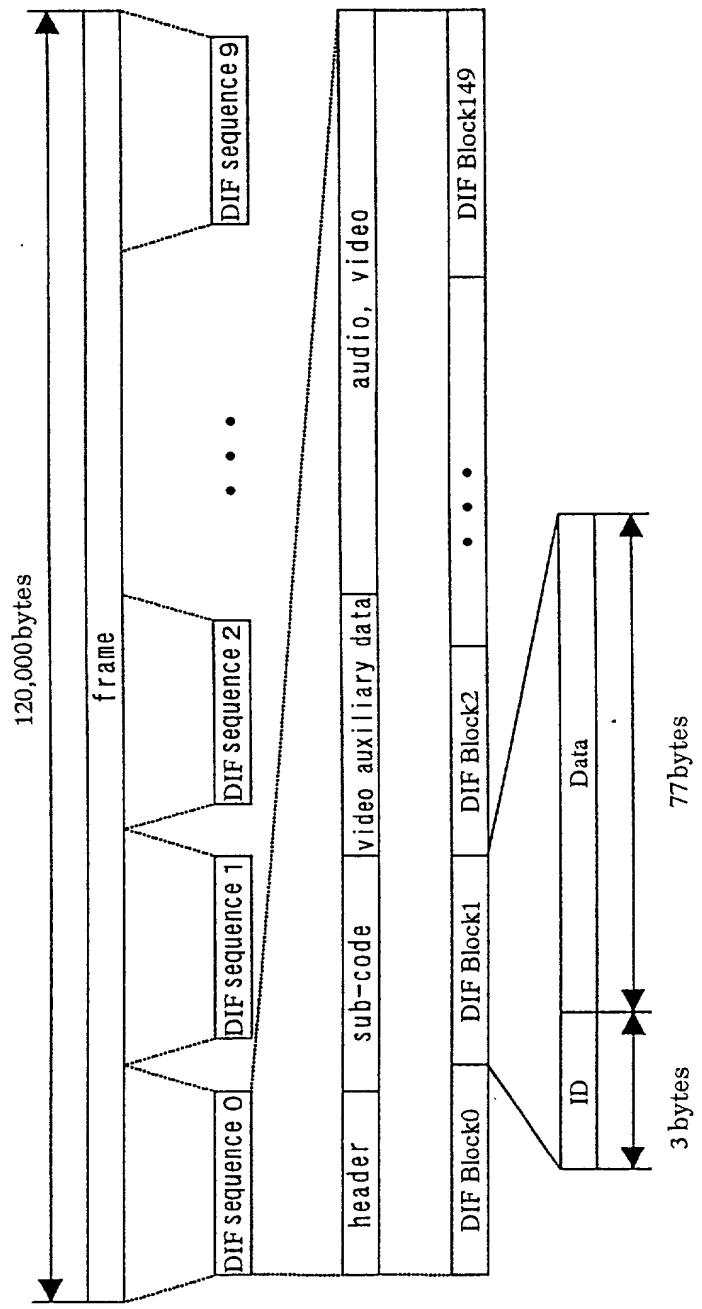


FIG. 26

